

## Claims

What we claim is:

1. A method for controlling power of a computer in which at least a power-on self test for hardware is carried out  
5 before shifting into an operating system process, when the power is turned on, comprising:

reading out a result of said power-on self test; and

- 10 turning on the power again after stopping the power supply to said computer when a predetermined test result has been read out.

2. The method according to Claim 1, wherein said predetermined test result includes a test result indicating that any hardware component contained in said computer is  
15 not initialized correctly.

3. The method according to Claim 1, wherein the operation of any hardware component contained in said computer is stabilized when said predetermined test result has been read out.

- 20 4. The method according to Claim 2, wherein the operation of any hardware component contained in said computer is stabilized when said predetermined test result has been read out.

5. The method according to Claim 3, wherein at least one selected from the group consisting of a setup for stabilizing the operation of a power circuit in said computer, a setup for cooling the inside of said computer, and a setup for disabling a function to suppress power consumption is carried out to stabilize the operation of said hardware component.

6. The method according to Claim 4, wherein at least one selected from the group consisting of a setup for stabilizing the operation of a power circuit in said computer, a setup for cooling the inside of said computer, and a setup for disabling a function to suppress power consumption is carried out to stabilize the operation of said hardware component.

7. The method according to Claim 1, wherein the power is inhibited to be turned on again after the power supply to said computer is stopped when said predetermined test result has been read out at a predetermined number of times.

8. The method according to Claim 5, wherein the power is inhibited to be turned on again after the power supply to said computer is stopped when said predetermined test result has been read out at a predetermined number of times.

9. The method according to Claim 6, wherein the power is inhibited to be turned on again after the power supply to said computer is stopped when said predetermined test result has been read out at a predetermined number of times.

10. A power control apparatus for controlling power of a computer in which at least a power-on self test for hardware is carried out before shifting into an operating system process, when the power is turned on, comprising:

5                   a readout unit for reading out a result of said power-on self test; and

10                   a control unit for controlling to turn on the power again after stopping the power supply to said computer when a predetermined test result has been read out by said readout unit.

11. The power control apparatus according to Claim 10, wherein said predetermined test result includes a test result indicating that any hardware component contained in said computer is not initialized correctly.

15           12. The power control apparatus according to Claim 10, wherein said control unit controls to carry out a setup for stabilizing the operation of any hardware component contained in said computer when said predetermined test result has been read out.

20           13. The power control apparatus according to Claim 11, wherein said control unit controls to carry out a setup for stabilizing the operation of any hardware component contained in said computer when said predetermined test result has been read out.

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14. The power control apparatus according to Claim 12,  
wherein said setup for stabilizing the operation of said  
hardware component is at least one selected from the group  
consisting of a setup for stabilizing the operation of a  
5 power circuit in said computer, a setup for cooling the  
inside of said computer, and a setup for disabling a  
function to suppress power consumption.

15. The power control apparatus according to Claim 13,  
wherein said setup for stabilizing the operation of said  
10 hardware component is at least one selected from the group  
consisting of a setup for stabilizing the operation of a  
power circuit in said computer, a setup for cooling the  
inside of said computer, and a setup for disabling a  
function to suppress power consumption.

16. The power control apparatus according to Claim 10,  
wherein said control unit inhibits the power to be turned on  
again after the power supply to said computer is stopped  
when said predetermined test result has been read out at a  
predetermined number of times.

17. The power control apparatus according to Claim 14,  
wherein said control unit inhibits the power to be turned on  
again after the power supply to said computer is stopped  
when said predetermined test result has been read out at a  
predetermined number of times.

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18. The power control apparatus according to Claim 15,  
wherein said control unit inhibits the power to be turned on  
again after the power supply to said computer is stopped  
when said predetermined test result has been read out at a  
5 predetermined number of times.

19. A computer, comprising:

a power control apparatus for controlling  
power of said computer according to Claim 10;

10 a power unit being controlled by said power  
control apparatus; and

a computer load operating on the power  
supplied by said power unit.

20. A computer, comprising:

15 a power control apparatus for controlling  
power of said computer according to Claim 16;

a power unit being controlled by said power  
control apparatus; and

a computer load operating on the power  
supplied by said power unit.

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21. A computer, comprising:

a power control apparatus for controlling  
power of said computer according to Claim 17;

5 a power unit being controlled by said power  
control apparatus; and

a computer load operating on the power  
supplied by said power unit.

22. A computer, comprising:

10 a power control apparatus for controlling  
power of said computer according to Claim 18;

a power unit being controlled by said power  
control apparatus; and

a computer load operating on the power  
supplied by said power unit.

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